U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Patterson et al., Computer Organization & Design: The Hardware/Software Interface, 1998, Morgan Kaufmann Publishers, Inc., pgs. B-34 to B-35.
	V	Knijnenburg et al., Branch Classification to Control Instruction Fetch in Simultaneous Multithreaded Architectures, International Workshop on Innovative Architecture for Future Generation High Performance Processors and Systems, 1/10/02, pgs.67-76.
	W	Roth et al., Speculative Data-Driven Multithreading, Seventh International Symposium on High-Performance Computer Architecture, 1/19/01, pgs.37-48.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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